

What is claimed:

1. The motherboard comprising:

a chipset for managing data transfers within the motherboard;

a scalable interconnect connected to said motherboard; and

a plurality of high-speed video card slots connected to said interconnect.
2. The motherboard of claim 1, further comprising a switch connected to said interconnect, wherein said switch distributes bandwidth from said interconnect to said plurality of high-speed video card slots.
3. The motherboard of claim 2, wherein said interconnect comprises a x16 connection, and wherein said switch distributes bandwidth from said x16 connection to two x16 video card slots.
4. The motherboard of claim 1, wherein said interconnect comprises at least a x32 connection.
5. The motherboard of claim 4, wherein said interconnect is divided into two or more x16 connections between the chipset and the plurality of high-speed video card slots.

6. The motherboard of claim 1, wherein said interconnect comprises at least a x16 connection, and wherein said interconnect is divided into a x8 connection between the chipset and each of said plurality of high-speed video card slots.

7. The motherboard of claim 1, wherein said interconnect comprises a connection having at least 24 lanes, and wherein said interconnect is divided into a x8 connection between the chipset and one of said plurality of high-speed video card slots and a x16 connection between the chipset and another of said plurality of high-speed video card slots.

8. The motherboard of claim 1, wherein a display area is divided into separate sections, one or more graphics processing units (GPUs) are dedicated to graphics processing related to each of the display sections; and said GPUs are connected to said high-speed video card slots.

9. A method for coupling two or more graphics controllers to a motherboard, the method comprising the steps of:

providing a scalable interconnect for handling data transfers on said motherboard;

dividing said scalable interconnect into multiple high-speed connections; and

routing each of said high speed connections to a separate video slot, whereby each of said graphics controllers is connected to one of said video slots.

10. The method of claim 9, wherein said interconnect comprises at least a x16 connection and each of said high-speed connections comprises at least a x8 connection.

11. The method of claim 9, wherein a switch is connected to said interconnect, said switch distributing data transfers between said interconnect and said high speed connections.

12. The method of claim 11, wherein said interconnect comprises a x16 connection, and wherein each of said video slots is a x16 video card slot.

13. The method of claim 9, wherein said interconnect comprises at least a x32 connection.

14. The method of claim 13, wherein each of said high-speed connections comprises at least a x16 connection.

15. The method of claim 9, wherein, wherein said interconnect comprises at least a x24 connection, and wherein one of said high-speed connections comprises at least a x8 connection and another of said high-speed connections comprises at least a x16 connection.

16. The method of claim 9, wherein:
a display area is divided into separate sections, and
one or more GPUs on said graphics controllers are dedicated to graphics processing related to each of the display sections.
17. The method of claim 9, wherein said method is agnostic to a specific chipset.
18. A computer comprising
a motherboard having multiple high-speed expansion slots, each of said slots being capable of at least a x8 connection; and
multiple graphics controllers, each connected to a separate expansion slot.
19. The computer of claim 18, wherein each of said slots is capable of at least a x16 connection.
20. The computer of claim 18, wherein one of said slots is capable of a x8 connection and another of said slots is capable of a x16 connection.
21. The computer of claim 18, wherein said motherboard further comprising a switch, said switch distributing data transfers to and from two or more of said high-speed expansion slots.

22. The computer of claim 21, wherein said interconnect comprises a x16 connection, and wherein each of said expansion slots is x16.

23. The computer of claim 18, wherein a display area is divided into separate sections and one or more graphics processing units (GPUs) on said graphics controllers is dedicated to graphics processing related to each of the display sections.

24. A device for coupling two or more graphics controllers to a motherboard, the device comprising:

a scalable interconnecting means for handling data transfers on said motherboard;

a dividing means for separating data transfers from said scalable interconnecting means to multiple high-speed connecting means; and

a routing means for connecting each of said high speed connections to a separate slotting means, wherein each of said graphics controllers is connected to one of said slotting means.

25. The device of claim 24, wherein said interconnecting means comprises a x16 connection, and wherein each of said high-speed connecting means comprises a x8 connection.

26. The device of claim 24, wherein said interconnecting means comprises a x32 connection, and wherein each of said high-speed connecting means comprises a x16 connection.

27. The device of claim 24, wherein said interconnecting means comprises a x24 connection, wherein one of said high-speed connecting means comprises a x8 connection, and wherein another of said high-speed connecting means comprises a x16 connection,

28. The method of claim 24 wherein said dividing means comprising a switching means for distributing data transfers between said interconnecting means and said high speed connecting means, wherein said interconnecting means comprises a x16 connection, and wherein each of said high-speed connecting means comprises a x16 connection.